

Notice of Allowability

Application No.

10/799,183

Examiner

Pierre E. Elisca

Applicant(s)

CAMPBELL ET AL.

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 1/9/2006.
2. ☒ The allowed claim(s) is/are 1-18.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

Pierre E. Elisca
PRIMARY EXAMINER

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REASONS FOR ALLOWANCE

1. This is an Examiner's statement of reasons for allowance. The closest prior art Davies, Andrew K U.S. Pat. No. 4,897,876 discloses a digital signal encryption using a cipher algorithm in which a multi-bit key signal is used to produce a first keystream. JP363039229A, Senoo, Masayuki discloses a secure decoding operation by providing a specific level to a status bit .

However, neither Davies nor Senoo singularly or in combination discloses the recited feature:

As per claims 1 and 12" comparing the attempt keystream bits to the current keystream bits, feeding back the current keystream bits as a new current seed in response to attempted keystream bits that equal the current keystream bits, and feeding back the attempted keystream bits as a new current seed in response to attempted keystream bits that do not equal the current keystream bits".

As per claim 7 " generating a new current set of test bits, the new current set of test bits being one of a plurality of sets of test bits, wherein no two sets of test bits have identical bit values, repeating steps (a)-(j) until each of the plurality of sets of test bits has been generated, in response to the attempted keystream bits being not equal to the current keystream bits and having generated each of the plurality of sets of test bits, receiving a new plurality of cipher bits, and repeating steps (a)-(m) until the attempted keystream bits are equal to the current keystream bits".

As per claim 18 " n function generators in the FPGA, each function generator having an output port coupled to a respective one of the registers and a plurality of input ports

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coupled to output ports of at least two of the registers, wherein each function generator is configured to apply an exclusive-or function to signals at the input ports, and each register is updated with a new state in response to output from the coupled function generator”.

Conclusion

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Pierre E. Elisca whose telephone number is 571 272 6706. The examiner can normally be reached on 6:30 to 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, James Trammell can be reached on 571 272 6712. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


Pierre Eddy Elisca

Primary Patent examiner

March 13, 2006